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Atty. Dkt. No. 039153-0642 (H0970)

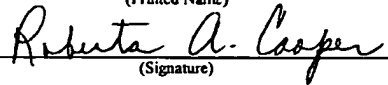
ITP
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2822

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Paton et al.

Title: OFFSET SPACER
PROCESS FOR FORMING
N-TYPE TRANSISTORS

(formerly "OFFSET
SPACER SMOS PROCESS
FOR FORMING N-TYPE
TRANSISTORS")

CERTIFICATE OF EXPRESS MAILING	
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Appl. No.: 10/619,877

Filing Date: 07/15/2003

Examiner: Christy L. Novacek

Art Unit: 2822

Confirmation No.: 7818

AMENDMENT AND REPLY UNDER 37 CFR 1.116

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Commissioner for Patents
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This communication is responsive to the Final Office Action dated September 23, 2004, concerning the above-referenced patent application.

Amendments to the Title are reflected on page 2 of this document.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this document.

Remarks/Arguments begin on page 8 of this document.

Please amend the application as follows:

11/24/2004 HALI11 00000038 10619877

01 FC:1202 18.00 OP
02 FC:1201 176.00 OP